

Electronic Acknowledgement Receipt

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Application Number:	10822275
International Application Number:	
Confirmation Number:	2177
Title of Invention:	System and method for optimizing interconnections of components in a multichip memory module
First Named Inventor/Applicant Name:	Jeffery W. Janzen
Customer Number:	27076
Filer:	Karen M. Lenaburg/Sherri Phillips
Filer Authorized By:	Karen M. Lenaburg
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Submitted with Payment	yes
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Amendment After Final	Response_03302009_2002-1152_00US.pdf	373998 cd37de7767a2b101ef9c4f8e7a6b46b91b919369	no	10
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed (SB/08)	SIDS_03302009_2002-1152_00US.pdf	101880 844641eddb45a7b6b490f73faa8f8d6ac3cd6ddfe	no	3
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	Fee Worksheet (PTO-06)	fee-info.pdf	30171 2cc87a88224507104da32255559d591ffda4bbe2	no	2
Warnings:					
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Total Files Size (in bytes):			506049		
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